

Application Note

SURFACE MOUNTING GRF342 RELAYS

Application Note T0180 provides suggested surface mounting information and printed circuit board [PCB] artwork for circuit traces and RF ground plane configurations. Suggested information provided is intended for use with Teledyne Relays' GRF342 surface mountable, ultraminiature, magnetic latching TO-5 relays.

The following pages give information for a printed circuit board layout and assembly process that will allow the the GRF342 to be tested in a manner similar to tests performed at Teledyne Relays and give a base line for production lay out.



Application Note

Introduction

The GRF342 is a surface mount, RF, magnetic latching relay that employs a butt lead attachment scheme. Normally this form of attachment method does not allow for easy grounding of the relay case which is very important as frequencies increase. To allow for surface mount and proper relay grounding, Teledyne Relays has developed the ground shield. The ground shield not only allows the relay to be easily grounded, it protects against inadvertent misalignment of the leads as well as isolating the leads from one another after mounting them to the board. To optimize the ground shield connection the following is presented to facilitate testing and layout for use in production:

Page 3 GRF342 PCB Layout for test:

- Circuit side
- Ground plane side

Page 4 GRF342 PCB General Layout:

- Circuit side trace
- Lead trace

Page 5 GRF342 Artwork:

- Soldermask
- Stencil and Aperture Size
- Relay outline

Page 6 GRF342 General Notes

RF characterization data presented in the GRF342 data sheet is de-embedded from the RF circuit test board.

Application Note

RF Test Boards

RF performance characteristics of the GRF342 was obtained by mounting the relays on test boards developed by Teledyne Relays. To the test boards, 6 female, 2 hole panel mount, solder cup contact SMA connectors were carefully soldered to the signal traces and the bodies soldered to the back side ground plane.

GRF342 ARTWORK CIRCUIT SIDE

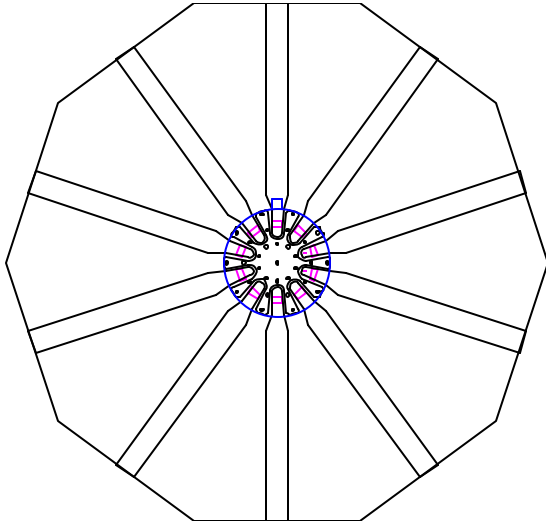


Figure 1

Figure 2 shows the ground plane side of the test board. There are 17 plated through vias connecting the ground trace on the component side of the board to the ground plane.

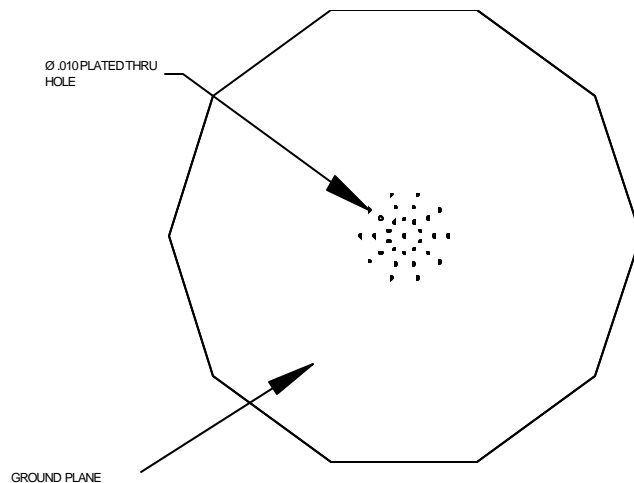
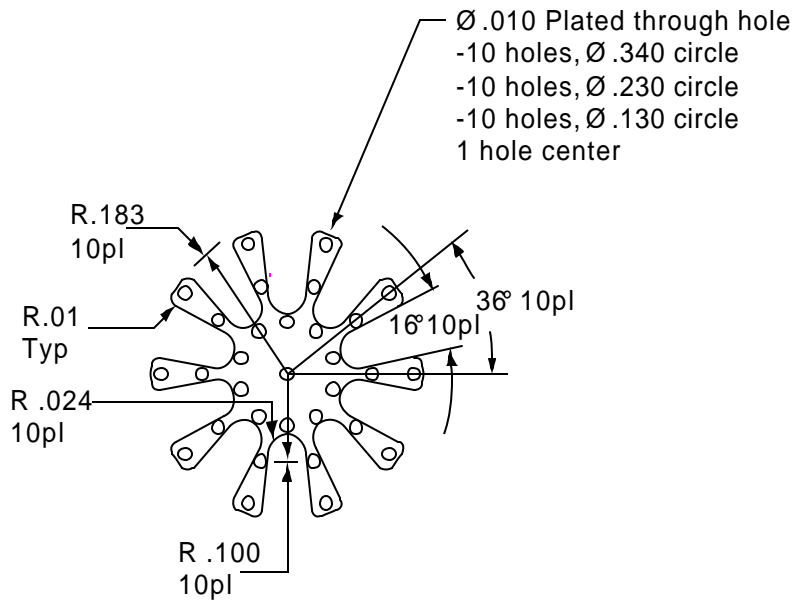


Figure 2

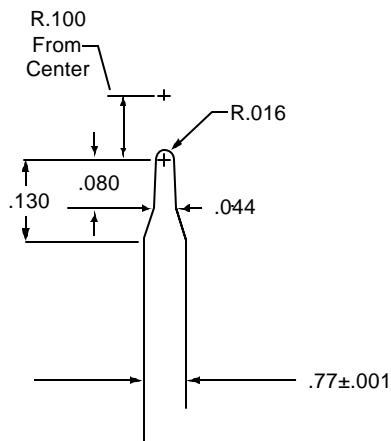
Application Note



Detail A

Detail A

Detail A defines the ground pad layout that will interface with the GRF342 ground shield. Also defined are the GRF342 relay pin connections as well as the relationship of the vias connecting the ground pad to the ground plane on the opposite side of the board. Via details are given in Figure 4.



Detail B

Detail B

Detail B defines the connection and trace width for the GRF342 signal pins. This is the definition of the trace that was used for RF performance characterization by Teldyne Relays.

Application Note

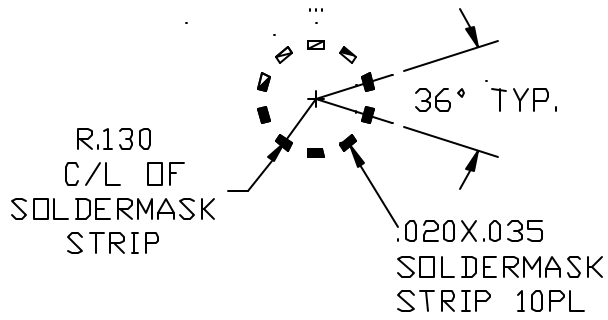


Figure 3

Solder Mask

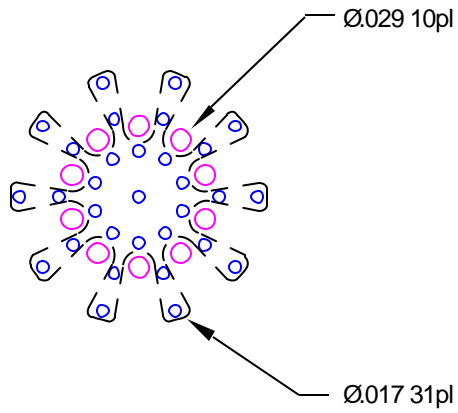


Figure 4

Stencil and Aperture Size

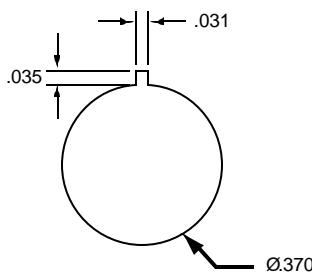


Figure 5

Relay Outline

Application Note

General Notes

GRF342 Printed Circuit Test Board Details

- a. Material: RT/duroid®6002 [RT/duroid®6002 is a registered trademark of Rogers Corporation]
- b. Thickness: 0.030"
- c. Copper foil thickness: 0.00134"
- d. All hole dimensions are after plate

- Suggested solder stencil thickness : 0.008"

- Trace configurations, board material, outline, size, etc. may require changes per user's application requirements.

- GRF342 relays may be subjected to solder reflow peak temperatures of 260°C maximum, for 1 minute, 3 times.

- Solder bottom of RF ground shield to PCB component side RF ground plane for best RF performance.

- Check with solder supplier for recommended solder reflow temperature profile for selected solderpaste and specific application requirements.

Reference data:

The following web sites may be useful to those unfamiliar with SMT technology:

<http://www.pegasustech.com/gems/sm.htm>

http://www.national.com/ms/MO/MOUNTING_OF_SURFACE_MOUNT_COMPONENTS-MISC.pdf